



AVIT
AARUPADAI VEEDU INSTITUTE OF TECHNOLOGY



VINAYAKA MISSION'S
RESEARCH FOUNDATION
(Deemed to be University under section 3 of the UGC Act 1956)



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

PROG, BRANCH,	B. E., E. C. E
YEAR/ SEMESTER, SECTION	II / IV, -
SUBJECT	17ECCC86 - SIGNAL PROCESSING LAB
ACADEMIC YEAR	2020-2021 (EVEN SEMESTER)

STANDARD OPERATING PROCEDURE

PERSONAL COMPUTER

Control Plan:

- Switch on the system properly.
- Close all the application and shut down the system.

Experimental Procedures:

- Select MATLAB software (**Start—MATLAB**) and run.
- Check the workspace and clear all the previous execution.
- Open the editor (**File\New\editor**).
- Type the Script.
- Run the programme (**Press F5 or File\Save & Execute**)
- If there is no error, it'll execute the script.
- If any errors, it'll be displayed in the Command Prompt. Read and Clear the errors.
- Run the programme (**Press F5 or File\Save & Execute**), If errors repeat the above steps till no errors.
- Save the results.


HOD/ ECE