



AVIT
AARUPADAI VEEDU INSTITUTE OF TECHNOLOGY



VINAYAKA MISSION'S
RESEARCH FOUNDATION
(Deemed to be University under section 3 of the UGC Act 1956)

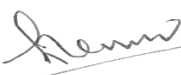


DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

PROG, BRANCH,	B. E., E. C. E
YEAR/ SEMESTER, SECTION	II / IV, -
SUBJECT	17ECCC86 - SIGNAL PROCESSING LAB
ACADEMIC YEAR	2020-2021 (EVEN SEMESTER)

LIST OF EXPERIMENTS

1. Generate different time signals and display the same.
2. Compute the linear convolution of a signal using DFT.
3. Compute the circular convolution of a given signal.
4. Design analog Chebyshev filters and apply bilinear transformation
5. Design analog Butterworth filters and apply bilinear transformation
6. Design analog Chebyshev filters and apply impulse invariance transformation
7. Design analog Butterworth filters and apply impulse invariance transformation
8. Design FIR filters using Fourier series method and frequency sampling methods
9. Design FIR filters using Different windowing techniques
10. Effect of quantizations


HOD/ ECE