VINAYAKA MISSION'S RESEARCH FOUNDATION
(Deemed to be University under section 3 of the UGC Act 1956)
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AVIT
AARUPADAI VEEDU INSTITUTE OF TECHNOLOGY
Vinayaka Mission's Chennai Campus

DIAMOND
I-GAUGE

Cordially invites you all to the

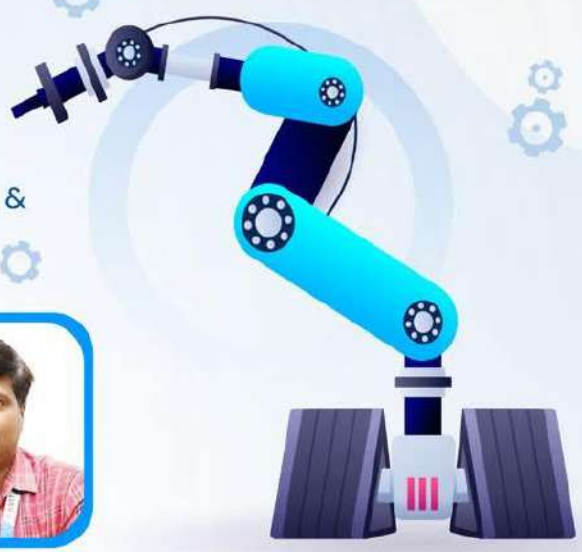
Value Added Course on

Industry 4.0

Digital twin for process industry (VA225)

Resource Persons

Dr. L. Chitra
Professor & Head
Department of Electrical & Electronics Engineering



Dr. S. Prakash
Assistant Professor (Gr-II)
Department of Electrical & Electronics Engineering

3rd – 7th April 2025 | Total Duration: 18 hours

Siemens Integrated Engineering Design Research Lab Classroom

Organized by: Department of Electrical & Electronics Engineering

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VINAYAKA MISSION'S RESEARCH FOUNDATION
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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

REPORT ON EVENT "VALUE ADDED COURSE: INDUSTRY 4.0 – DIGITAL TWIN FOR PROCESS INDUSTRY (VA225)"

Date: 3rd to 7th April 2025

The Department of Electrical and Electronics Engineering at Aarupadai Veedu Institute of Technology (AVIT), Vinayaka Mission's Research Foundation (VMRF), proudly organized a Value Added Course (VAC) titled *"Industry 4.0 – Digital Twin for Process Industry (VA225)"*. The course was conducted from 3rd to 7th April 2025, with a total duration of 18 hours, at the Siemens Integrated Engineering Design Research Lab Classroom.

The course aimed to provide students with a deep insight into the rapidly evolving domain of Industry 4.0, with a specific focus on Digital Twin technology and its transformative impact on process industries. The concept of digital twins—virtual representations of physical systems—has become a cornerstone in achieving smart, automated, and data-driven industrial operations.

Resource Persons:

Dr. L. Chitra, Professor & Head, Department of Electrical and Electronics Engineering

Dr. S. Prakash, Assistant Professor (Grade II), Department of Electrical and Electronics Engineering

These distinguished faculty members guided the participants through theoretical foundations, practical applications, and real-time case studies related to digital twins and Industry 4.0. Their sessions enabled students to explore modern industrial challenges and the role of emerging technologies in addressing them.

The VAC was designed not only to bridge the gap between academic learning and industrial requirements but also to equip students with essential skills for future employment and research in smart manufacturing and process automation.

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