



**VINAYAKA MISSION'S
RESEARCH FOUNDATION**
(Deemed to be University under section 3 of the UGC Act 1956)



AVIT
AARUPADAI VEEDU INSTITUTE OF TECHNOLOGY

*Department of Electrical and Electronics Engineering
in association with Institution of Engineers (India)*



*Cordially invites you all for the
Employability Enhancement Course on*

Grid Connected and Standalone PV Solar Plant design using PVSYS

26th to 28th May 2022 @ 09.00 a.m.

Venue : Integrated Engineering Design Research Lab

Resource persons

Mr. M. Pradeep
Senior Power System Engineer
Power Projects, Chennai

Mr. P. Kathirmani Selvan
Power System Engineer
Power Projects, Chennai

In the presence of

Dr. G. Selvakumar, Principal

Convener : **Dr. L. Chitra**, HoD, EEE
Event Coordinators : **Mr. S. Prakash**, AP (Gr-II), EEE
Ms. K.S. Kavitha Kumari, AP (Gr-II), EEE