



**AARUPADAI VEEDU
INSTITUTE OF TECHNOLOGY**
(An Constituent College of Vinayaka Mission's Research Foundation)



**VINAYAKA MISSION'S
RESEARCH FOUNDATION**
(Deemed to be University under section 3 of the UGC Act 1956)



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AVIT

**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
IN ASSOCIATION WITH
THE INSTITUTION OF ENGINEERS (INDIA) - STUDENT CHAPTER**

Cordially invites you all for the

**VALUE ADDED TRAINING PROGRAMME ON
"INDOOR AND OUTDOOR ILLUMINATION
ENGINEERING DESIGN USING DIALUX"**



Date : 30th Jan to 03rd Feb 2020

Venue: Digital Hall – II

Time: 10.00 AM

Chief Guest

MS. C.R.AMUTHA
Manager,
Power System Studies,
Power Projects

Presided By

Dr. S.A.V.SATYA MURTY
Campus Director, AV Campus
Director Research, VMRF

Special Address By

Dr. K. L. SHUNMUGANATHAN
Principal, AVIT

Felicitation

Dr. D. VIJENDRA BABU
VP(Part Time Studies), AVIT

Prof. P. RAJASEKARAN
VP(Admin.), AVIT

Dr. S. P. SANGEETHA
VP(Academics), AVIT

With warm regards

Mr. S. PRAKASH
AP (Gr-II) / EEE & Coordinator

Mr. N. P. GOPINATH
AP (Gr-II) / EEE & Coordinator

Dr. L.CHITRA
Associate Professor & HoD / EEE



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AGENDA

10.00 am : Thamizh thai vazhthu

10.05 am : Welcome Address

Dr. L. Chitra

Associate Professor & HoD / EEE

10.10 am : Presidential Address

Dr. S.A.V. Satya Murty

Campus Director, AV Campus
Director Research, VMRF

10.15 am : Special Address

Dr. K. L. Shunmuganathan

Principal, AVIT

10.20 am : Felicitation

Prof. P. Rajasekaran

VP(Admin.), AVIT

10.30 am : Chief guest Address

Ms. C.R.Amutha

Manager, Power System Studies,
Power Projects