

AARUPADAI VEEDU INSTITUTE OF TECHNOLOGY

VINAYAKA MISSIONS UNIVERSITY

DEPARTMENT OF ECE

Report – Guest Lecture on “Advanced VLSI Design Technique”

Department of Electronics & Communication Engineering, IEEE SB Chapter, Aarupadai Veedu Institute of Technology organized a **Guest Lecture on “Advanced VLSI Design Techniques”** on **04th Aug, 2016**. Prof.D.Vijendra Babu, Vice Principal & HOD/ECE delivered the Welcome address. Dr.S.Suyambazhahan, Principal, AVIT delivered the Presidential address. Mr.B.Sivachandra Mahalingam, Organizing Member introduced the chief guest of the function. The Chief Guest Dr.D.Meganathan, Assistant Professor, Department of Electronics Engineering, MIT Campus, Anna University delivered the key note address and started his lecture with future trends in electronics and deeply discussed about the advancement in VLSI design and its applications. Students from ECE, BME & EEE participated in this guest lecture and gained the knowledge in VLSI Design Methodologies. Mrs.L.K.Hema Associate Professor, ECE delivered the Vote of thanks.

